

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
12 February 2004 (12.02.2004)

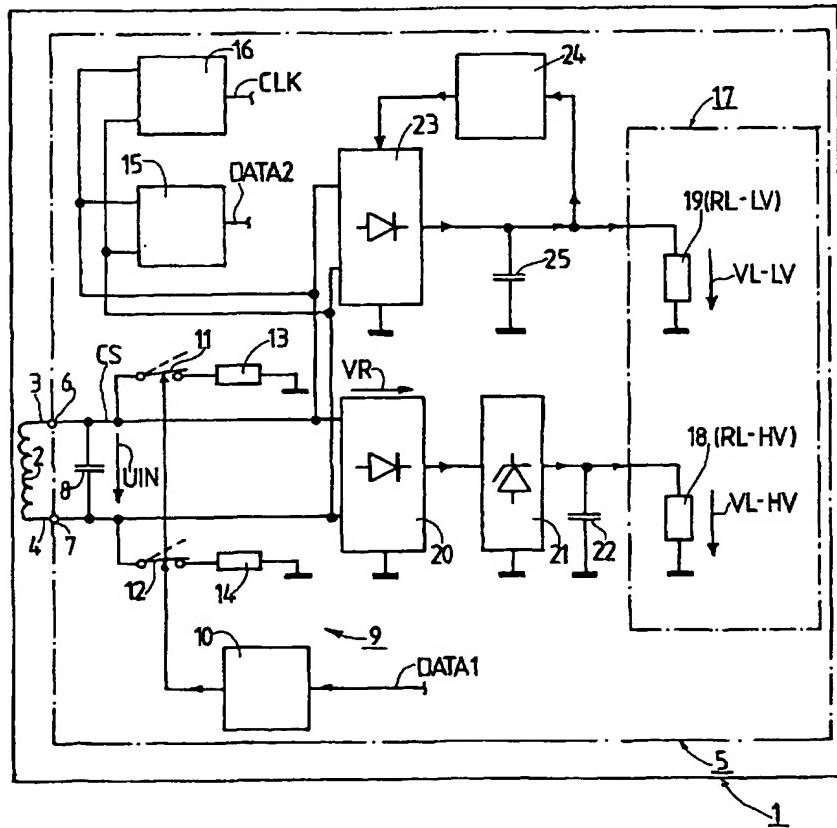
PCT

(10) International Publication Number
WO 2004/013807 A1

- (51) International Patent Classification⁷: **G06K 19/07**
- (21) International Application Number: PCT/IB2003/003279
- (22) International Filing Date: 18 July 2003 (18.07.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data: 02102075.5 30 July 2002 (30.07.2002) EP
- (71) Applicant (for all designated States except US): KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 RA Eindhoven (NL).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): KOFLER, Robert [AT/AT]; Triester Strasse 64, A-1101 Vienna (AT).
- (74) Agent: WEBER, Helmut; Philips Intellectual Property & Standards, Triester Strasse 64, A-1101 Vienna (AT).
- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: TRANSPONDER WITH TWO SUPPLY VOLTAGES



(57) Abstract: In a transponder (1) and an integrated circuit (5), the integrated circuit (5) has two circuit sections (18, 19) that are arranged for supply with two supply voltages of different levels (VL-HV, VL-LV), a first rectifier circuit (20) and a limiter stage (21) connected downstream of the first rectifier circuit (20) being provided, from which limiter stage (21) the higher, first supply voltage (VL-HV) for the first circuit section (18) can be picked off, and a second rectifier circuit (23) and a control stage (24) to control said second rectifier circuit (23) being provided, from which second rectifier circuit (23) the lower, second supply voltage (VL-LV) for the second circuit section (19) can be picked off without passing through an intervening limiter stage.



Published:

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Transponder with two supply voltages

The invention relates to a transponder that is arranged for non-contacting communication with a communication station and that has transmission means and that has an integrated circuit with circuit connecting contacts, wherein the transmission means are connected to the circuit connecting contacts and an input voltage can be picked off from said circuit connecting contacts, wherein first rectifier means and limiter means cooperating with said first rectifier means are provided, wherein a voltage representing the input voltage can be fed to the first rectifier means, and wherein a first supply voltage can be picked off from the first rectifier means or from the limiter means.

The invention further relates to an integrated circuit that is intended for use in a transponder for non-contacting communication with a communication station, which transponder is arranged as detailed in the first paragraph.

A transponder of the kind detailed in the first paragraph and an integrated circuit of the kind detailed in the second paragraph have become commercially available in a number of variant versions and are therefore known. In connection with a transponder of this kind, reference may also be made to US patent No. 6,168,083 B1.

In the known transponder that has been put on the market, the design of the integrated circuit is such that only the first supply voltage is required for supplying the integrated circuit, which first supply voltage is generated by means of the first rectifier means and the limiter means connected downstream of the first rectifier means, use being made for this purpose of the input voltage that arises at the circuit connecting contacts, which input voltage originates from a load-modulated carrier signal when the transponder is in a transmitting mode and from an amplitude-modulated carrier signal when it is in a receiving mode.

In the field of transponders, the direction in which development is moving is that, to produce an integrated circuit for a transponder of this kind, increasing frequent use is being made of integration processes involving shorter and shorter channels lengths, which has the advantage that even circuits of relatively complicated design can be produced in the

form of integrated circuits that are very small in area and hence inexpensive. However, going hand in hand with this is the fact that, compared with what was true earlier and hitherto, it is only lower maximum supply voltages that are permitted for a part of an integrated circuit but there are also circuit components, and particularly memories such as an EEPROM, present in an integrated circuit of this kind that still require a relatively high supply voltage. In the known data carrier, the relatively low supply voltage could be generated by generating it in a separate supply-voltage generating circuit having a rectifier circuit and a limiter circuit connected downstream of the rectifier circuit, but this meant that only a relatively low input voltage could be obtained at the circuit connecting contacts, which resulted in a reduced modulation spectrum and hence a reduced range of communication when the transponder was in the transmitting mode, which is of course undesirable.

It is an object of the invention to solve the above problems in an easy way and to produce an improved transponder and an improved integrated circuit for such a transponder.

To achieve the object stated above, features according to the invention are provided in a transponder according to the invention such that a transponder according to the invention can be characterized as follows, namely:

A transponder that is arranged for non-contacting communication with a communication station and that has transmission means and that has an integrated circuit with circuit connecting contacts, wherein the transmission means are connected to circuit connecting contacts and an input voltage can be picked off from said circuit connecting contacts, wherein the integrated circuit has a first circuit section and a second circuit section, wherein the first circuit section is arranged for being supplied with a first supply voltage and the second circuit section is arranged for being supplied with a second supply voltage, wherein first rectifier means and limiter means cooperating with said first rectifier means are provided, wherein a voltage representing the input voltage can be fed to the first rectifier means, wherein the first supply voltage can be picked off from the first rectifier means or from the limiter means, wherein second rectifier means and control means for controlling said second rectifier means are provided, wherein a voltage representing the input voltage can also be fed to the second rectifier means, wherein the second supply voltage can be picked off from the second rectifier means, and wherein the value of the second supply voltage that can be picked off from the second rectifier means can be controlled by the control means.

To achieve the object stated above, features according to the invention are provided in an integrated circuit according to the invention such that an integrated circuit according to the invention can be characterized as follows, namely:

An integrated circuit that is intended for use in a transponder for non-contacting communication with a communication station and that has circuit connecting contacts that are intended for connection to transmission means of the transponder, from which contacts an input voltage can be picked off, and that has a first circuit section and a second circuit section, wherein the first circuit section is arranged for being supplied with a first supply voltage and the second circuit section is arranged for being supplied with a second supply voltage, wherein first rectifier means and limiter means cooperating with said first rectifier means are provided, wherein a voltage representing the input voltage can be fed to the first rectifier means, wherein the first supply voltage can be picked off from the first rectifier means or from the limiter means, wherein second rectifier means and control means for controlling said second rectifier means are provided, wherein a voltage representing the input voltage can also be fed to the second rectifier means, wherein the second supply voltage can be picked off from the second rectifier means, and wherein the value of the second supply voltage that can be picked off from the second rectifier means can be controlled by the control means.

What is achieved by the provision of the features according to the invention, in a manner which is simple in terms of circuitry, is that, in a transponder according to the invention and in an integrated circuit according to the invention, both a higher, first supply voltage and a lower, second supply voltage can be generated and that, despite the generation of the lower, second supply voltage, a wide modulation spectrum is ensured that is independent of the lower, second supply voltage and is dependent only on the higher, first supply voltage obtained by means of the limiter means, and a long range of communication is thus ensured when the transponder is in a transmitting mode. The generation of the lower, second supply voltage also produces the major advantage that the section of the circuit that is arranged for supply with the lower, second supply voltage also draws only a smaller supply current, which means that, overall, the power consumption of this section of the circuit, and hence of the integrated circuit and of the transponder, is low. The generation of the lower, second supply voltage also produces another advantage, namely that a back-up capacitor that is produced by integrated circuit technology and at which this lower, second supply voltage arises can be made relatively small in terms of area.

In a transponder according to the invention and in an integrated circuit according to the invention, the control means may be arranged to control the value, i.e. the amplitude, of the lower, second supply voltage as a function of the value, i.e. the amplitude, of the input voltage that can be picked off from the circuit connecting contacts. It has however proved particularly advantageous if, in a transponder according to the invention and in an integrated circuit according to the invention, the features claimed in claim 2 and in claim 4 are also provided in the respective cases. An arrangement of this kind has proved advantageous with regard to particularly accurate control of the value of the lower, second supply voltage. The output voltage or the output current from the second rectifier means can be used as an output variable in this case.

These and other aspects of the invention are apparent from and will be elucidated with reference to the embodiment described hereinafter, though the invention is not limited to this embodiment.

In the drawings:

Fig. 1 is a block circuit diagram that shows, in a highly diagrammatic form, a part of a transponder, and of an integrated circuit for the said transponder, that is essential in the present connection, according to one embodiment of the invention.

Fig. 1 shows a transponder 1. The transponder 1 is in the form of a tag or label. The transponder 1 may however also be in the form of a card-like data carrier. The transponder 1 is intended and arranged for non-contacting communication with a communication station (not shown). For this purpose, the transponder 1 has transmission means 2 that are formed in the present case by a transmission coil 2 that, in an inductive manner, i.e. in the manner employed in a transformer, can enter into working connection with a transmission coil of the communication station (not shown), to allow a transmission to be made. Transmission means that operate capacitively may be provided in place of the transmission coil 2. The transmission means may also be formed by a dipole or monopole, particularly when transmission takes place at very high frequencies in the MHz or GHz range. The transmission coil 2, i.e. the transmission means 2, has a first transmission-means connecting contact 3 and a second transmission-means connecting contact 4. The transmission means may also have more than two transmission-means connecting contacts.

The transponder 1 also contains an integrated circuit 5. The integrated circuit 5 has a first circuit connecting contact 6 and a second circuit connecting contact 7. Other circuit connecting contacts are not shown. The first circuit connecting contact 6 has an electrically conductive connection to the first transmission-means connecting contact 3 and the second circuit connecting contact 7 has an electrically conductive connection to the second transmission-means connecting contact 4. Because a transmission coil 2 is provided as the transmission means in the present case, there is connected to the two circuit connecting contacts 6 and 7 a capacitor 8 that is produced in the integrated circuit 5 and that forms a resonant circuit with the transmission coil 2. The capacitor 8 may also be provided outside the integrated circuit 5. The resonant frequency of the resonant circuit is matched in the present case to the frequency of a carrier signal CS, but this need not necessarily be the case. When the transponder 1 is in a transmitting mode, the carrier signal CS is received from the communication station (not shown) in an unmodulated form and is load-modulated by the transponder 1. When the transponder 1 is in a receiving mode, the carrier signal CS is emitted in amplitude-modulated form by the communication station (not shown). Rather than amplitude modulation, use may also be made of frequency modulation or phase modulation. Both in its load-modulated form and in its amplitude-modulated form and its unmodulated form, the carrier signal CS produces an input voltage UIN that can be picked off from the two circuit connecting contacts 6 and 7.

To allow the unmodulated carrier signal CS to be load-modulated, the transponder 1 and the integrated circuit 5 have load-modulating means 9 that are connected to the two circuit connecting contacts 6 and 7. The load-modulating means 9 have a load-modulation control circuit 10 and two electronic switches 11 and 12 that can be controlled by the load-modulation control circuit 10, with a first resistor 13 being connected in series with the first switch 11 and a second resistor 14 being connected in series with the second switch 12. First data DATA1 in coded form can be fed to the load-modulation control circuit 10, which results in the load-modulation control circuit 10 exerting control on the two switches 11 and 12 as a function of the first data DATA1 fed to it, which causes the two switches 11 and 12 to open and close as a function of the first data DATA1, which in turn results in the unmodulated carrier signal CS being load-modulated by the opening and closing of the two switches and by the switching in and out of the two resistors 13 and 14 that occurs as a result of this. There are other possible designs of circuit for effecting the load-modulation, such for example as one having only one electronic switch and one resistor or having a capacitor instead of the resistor.

To allow an amplitude-modulated carrier signal CS that is received by means of the transmission coil 2 to be demodulated, the transponder 1 and the integrated circuit 5 have a demodulating circuit 15 that is likewise connected to the two circuit connecting contacts 6 and 7. Demodulation of the amplitude-modulated carrier signal CS can be performed by means of the demodulating circuit 15, which results in the demodulating circuit 15 emitting second data DATA2, this second data DATA2 still being in coded form.

Also connected to the two circuit connecting contacts 6 and 7 is a clock-signal regeneration circuit 16 by means of which a clock signal CLK can be regenerated from the carrier signal CS. It is however also possible for a clock-signal generator, with which a clock signal can be generated independently of a carrier signal, to be provided in place of the clock-signal regeneration circuit 16.

The transponder 1 and the integrated circuit 5 also contain a microcomputer 17. A hard-wired logic circuit may also be provided in place of the microcomputer 17. The microcomputer 17 contains memory means (not shown). The microcomputer 17 is intended and arranged to process first data DATA1 to be read out, which data DATA1 is stored in the memory means (not shown), and to process second data DATA2 to be stored. The microcomputer 17 contains a first circuit section 18 and a second circuit section 19. The two circuit sections 18 and 19 are intended and arranged to process data or signals, with the first circuit section 18 being intended and arranged mainly, but not exclusively, to process analog signals and the second circuit section 19 being intended and arranged mainly, but not exclusively, to process digital signals. The first circuit section 18 and the second circuit section 19 each comprise a large number of circuit assemblies. The first circuit section 18 may contain the above-mentioned memory means for example. The two circuit sections 18 and 19 are merely shown diagrammatically in Fig. 1 in the form of load resistors having resistances RL-HV and RL-LV respectively.

In the transponder 1 and the integrated circuit 5, the first circuit section 18 is arranged for being supplied with a first supply voltage VL-HV. The second circuit section 19 is arranged for being supplied with a second supply voltage VL-LV. In most of the circumstances under which the transponder 1 operates, namely when the transponder 1 is relatively close to a communication station and is communicating therewith, the second supply voltage VL-LV is lower in this case than the first supply voltage VL-HV.

To generate the first supply voltage VL-HV that is higher in most of the circumstances under which the transponder 1 operates, the transponder 1 and the integrated circuit 5 have first rectifier means 20 and limiter means 21 connected downstream of said

first rectifier means 20. Connected downstream of the limiter means 21 in this case is a first storage capacitor 22 that acts as a means of storing energy. An arrangement where there is no such storage capacitor may also be implemented. The first rectifier means 20 comprise a bridge rectifier in this case but they may also be implemented in some other way. The limiter means 21 are implemented by using a Zener diode. Limiter means 21 of this kind have long been known. Such limiter means may also take the form of a so-called shunt regulator. A voltage representing the input voltage UIN can be fed to the first rectifier means 20, the input voltage UIN being fed directly to the rectifier means 20 in the transponder 1 shown in Fig. 1. This need not necessarily be the case however, because a voltage that is reduced in comparison with the input voltage UIN may also be fed to the first rectifier means 20. The input voltage UIN is rectified by the first rectifier means 20, after which limitation to the desired higher, first supply voltage VL-HV takes place by means of the limiter means 21. In the transponder 1 and the integrated circuit 5, the maximum voltage UIN at the circuit connecting contacts 6 and 7 is determined essentially by the sum of the voltage drop VR across the first rectifier means 20 and the higher, first supply voltage VL-HV. In the case of the solution that was produced in the course of developing the transponder 1 and the integrated circuit 5, the maximum higher, first supply voltage was selected to be approximately 5.5 V, thus giving, with a voltage drop of approximately 1 V across the first rectifier means 20, a maximum input voltage UIN of 6.5 V, namely in the case where the input voltage UIN is produced by the unmodulated carrier signal CS. When the transponder 1 is in a transmitting mode, the unmodulated carrier signal is, as already mentioned, subjected to load modulation by the load-modulating means 9, a load then being applied to the unmodulated carrier signal CS by the resistors 13 and 14 when the switches 11 and 12 are closed, which results in a reduced amplitude being obtained for the carrier signal CS. The reduced amplitude of the carrier signal CS is independent in this case of the amplitude of the unmodulated carrier signal CS. Due to the fact that the unmodulated carrier signal CS, and hence the input voltage UIN, is higher than the higher, first supply voltage VL-HV by the voltage drop VR across the first rectifier means 20, a relatively high input voltage UIN can be obtained in the case of an unmodulated carrier signal CS, which in turn results in a high modulation spectrum. A high modulation spectrum of this kind has the advantage that communication by the transponder 1 with a communication station becomes possible over a relatively long communication range.

To generate the second supply voltage VL-LV that is lower in most of the circumstances under which the transponder 1 operates, the transponder 1 and the integrated

circuit 5 have second rectifier means 23, and control means 24 to control said second rectifier means 23. Connected to the output of the second rectifier means 23 is a second storage capacitor 25 that is intended for energy storage purposes. The second rectifier means 23 comprise controlled diodes in this case. The second rectifier means 23 may however also be implemented in some other way, such as with a controlled bridge rectifier for example. A voltage representing the input voltage UIN can also be fed to the second rectifier means 23, the input voltage UIN being fed directly to the second rectifier means 23 in this case too. In the case of the second rectifier means 23 too, this need not necessarily be the case, because a voltage that is reduced, or even raised, in comparison with the input voltage UIN may also be fed to the second rectifier means 23. A raised voltage of this kind may for example be generated by means of a voltage doubling circuit. The lower, second supply voltage VL-LV is generated by the second rectifier means 23. The lower, second supply voltage VL-LV can be picked off from the second rectifier means 23 and fed to the second circuit section 19 without passing through any intervening limiter means, i.e. unlike the normally higher, first supply voltage VL-HV. The control means 24 are arranged in the present case to control the value, i.e. the amplitude, of the lower, second supply voltage VL-LV as a function of the value, i.e. the amplitude of the lower, second supply voltage VL-LV that arises at the output of the second rectifier means 23. By means of the control means, the second rectifier means 23 are controlled in such a way that the amplitude of the second supply voltage VL-LV that can be picked off from the second rectifier means 23 is controlled, this control being such that the usually lower, second supply voltage VL-LV always lies within a given range of voltages.

Due to the fact that the second rectifier means 23 do not have any limiter means connected downstream of them, the advantage is obtained that the lower, second supply voltage VL-LV cannot have any disadvantageous, namely reducing, effect on the input voltage UIN, which means that the input voltage UIN is dependent only on the higher, first supply voltage VL-HV. This gives the major advantage in the case of the transponder 1 and the integrated circuit 5 that the second circuit section 19 can be supplied with a lower, second supply voltage VL-LV, which is advantageous with regard to having a power consumption that is as low as possible, but that a high modulation spectrum can be obtained in the region of the circuit connecting contacts 6 and 7, which means that the transponder 1 has a long range of communication that is determined essentially by the higher, first supply voltage VL-HV and is not adversely, namely reducingly, affected by the lower, second supply voltage VL-LV.

It should be explicitly mentioned that operating situations may also occur in the transponder 1 and in the integrated circuit 5 in which the voltage of the usually lower, first supply voltage VL-LV is of a higher value than that of the usually higher, first supply voltage VL-HV. This happens particularly when the transponder 1 is communicating with a first communication station from relatively far away from the communication station, in which case there is then only a relatively low input voltage.

Only a first circuit section 18 and a second circuit section 19 are provided in the transponder 1 and the integrated circuit 5 that have been described with reference to Fig. 1. It should be mentioned that a transponder of this kind may also comprise at least one further circuit section, which has to be supplied with a further supply voltage that is lower than the first supply voltage VL-HV. A supply voltage of this kind may for example be generated by using the second supply voltage VL-LV, by means of a so-called in-phase regulator.

In the transponder 1 shown in Fig. 1 and the integrated circuit 5 shown in Fig. 1, the limiter means 21 cooperating with the first rectifier means 20 are connected downstream of the first rectifier means 21. This need not necessarily be the case, because it is also possible to have designs of circuit in which the limiter means are connected upstream of the rectifier means, in which case the higher, first supply voltage VL-HV is then picked off from the rectifier means.

CLAIMS:

1. A transponder (1) that is arranged for non-contacting communication with a communication station and that has transmission means (2) and that has an integrated circuit (5) with circuit connecting contacts (6, 7), wherein the transmission means are connected to the circuit connecting contacts (6, 7) and an input voltage (UIN) can be picked off from said circuit connecting contacts, wherein the integrated circuit (5) has a first circuit section (18) and a second circuit section (19), wherein the first circuit section (18) is arranged for being supplied with a first supply voltage (VL-HV) and the second circuit section (19) is arranged for being supplied with a second supply voltage (VL-LV), wherein first rectifier means (20) and limiter means (21) cooperating with said first rectifier means (20) are provided, wherein a voltage (UIN) representing the input voltage (UIN) can be fed to the first rectifier means (20), wherein the first supply voltage (VL-HV) can be picked off from the first rectifier means (20) or from the limiter means (21), wherein second rectifier means (23) and control means (24) for controlling said second rectifier means (23) are provided, wherein a voltage (UIN) representing the input voltage (UIN) can also be fed to the second rectifier means (23), wherein the second supply voltage (VL-LV) can be picked off from the second rectifier means (23), and wherein the value of the second supply voltage (VL-LV) that can be picked off from the second rectifier means (23) can be controlled by the control means (24).
2. A transponder (1) as claimed in claim 1, wherein the control means (24) are arranged to control the value of the second supply voltage (VL-LV) as a function of the value of the output variable (VL-LV) arising at the output of the second rectifier means (23).
3. An integrated circuit (5) that is intended for use in a transponder (1) for non-contacting communication with a communication station and that has circuit connecting contacts (6, 7) that are intended for connection to transmission means (2) of the transponder (1), from which contacts (6, 7) an input voltage (UIN) can be picked off, and that has a first circuit section (18) and a second circuit section (19), wherein the first circuit section (18) is arranged for being supplied with a first supply voltage (VL-HV) and the second circuit section (19) is arranged for being supplied with a second supply voltage (VL-LV), wherein

first rectifier means (20) and limiter means (21) cooperating with said first rectifier means (20) are provided, to which first rectifier means (20) a voltage (UIN) representing the input voltage (UIN) can be fed and from which limiter means (21) the first supply voltage (VL-HV) can be picked off, wherein second rectifier means (23) and control means (24) for controlling said second rectifier means (23) are provided, to which second rectifier means (23) a voltage (UIN) representing the input voltage (UIN) can also be fed and from which second rectifier means (23) the second supply voltage (VL-LV) can be picked off, and by which control means (24) the amplitude of the second supply voltage (VL-LV) that can be picked off from the second rectifier means (23) can be controlled.

4. An integrated circuit (5) as claimed in claim 3, wherein the control means (24) are arranged to control the amplitude of the second supply voltage (VL-LV) as a function of the amplitude of the output variable (VL-LV) arising at the output of the second rectifier means (23).

1/1

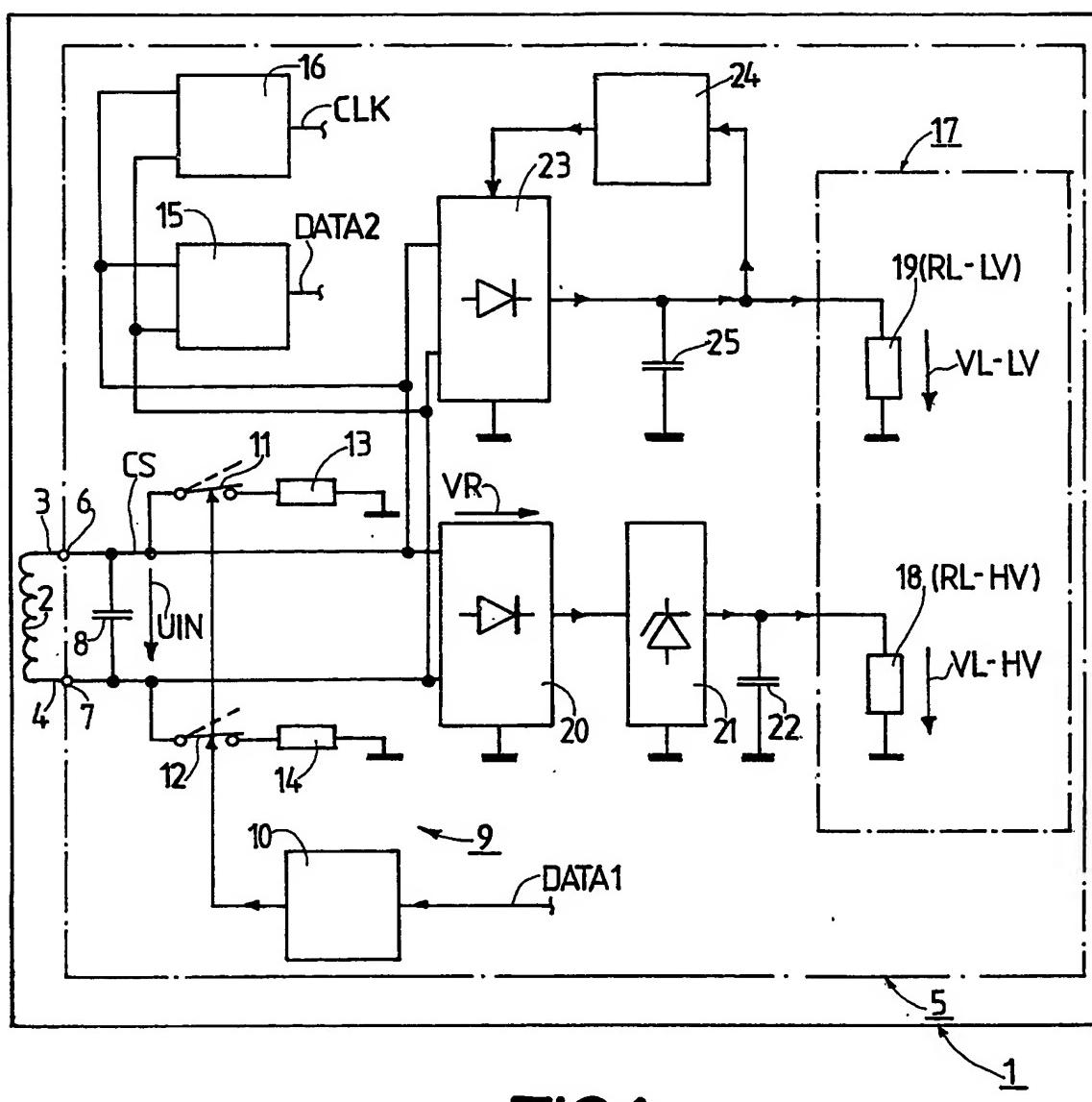


FIG.1

INTERNATIONAL SEARCH REPORT

International Application No
PCT/IB 03/03279A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06K19/07

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G06K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
P, X	EP 1 306 741 A (FUJITSU LTD) 2 May 2003 (2003-05-02) paragraphs '0010!-'0013!; figure 1 ---	1, 3
X	US 2002/054653 A1 (ZETTLER WERNER) 9 May 2002 (2002-05-09) paragraphs '0023!, '0024! ---	1, 3
X	US 2001/052798 A1 (MUEHLBERGER ANDREAS ET AL) 20 December 2001 (2001-12-20) paragraphs '0019!-'0022!, '0039!; figure 1 ---	1, 3
X	US 6 304 613 B1 (KOLLER MANFRED ET AL) 16 October 2001 (2001-10-16) column 7, line 40 -column 8, line 57; figures 1-4 ---	1, 3 -/-

 Further documents are listed in the continuation of box C. Patent family members are listed in annex.

* Special categories of cited documents :

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

- *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
- *&* document member of the same patent family

Date of the actual completion of the international search

12 November 2003

Date of mailing of the International search report

02/12/2003

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax. (+31-70) 340-3016

Authorized officer

Schauler, M

INTERNATIONAL SEARCH REPORT

International Search Report
PCT/IB 03/03279

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 874 327 A (SAMSUNG ELECTRONICS CO LTD) 28 October 1998 (1998-10-28) page 4, line 2-25; figure 3 -----	1,3
A	EP 1 094 604 A (ST MICROELECTRONICS SA) 25 April 2001 (2001-04-25) paragraph '0012!; figure 2 -----	1,3

INTERNATIONAL SEARCH REPORT

International Search Report
PCT/IB 03/03279

Patent document cited in search report		Publication date		Patent family member(s)		Publication date
EP 1306741	A	02-05-2003	JP EP US	2003132316 A 1306741 A2 2003083037 A1		09-05-2003 02-05-2003 01-05-2003
US 2002054653	A1	09-05-2002	WO EP	0211060 A1 1214684 A1		07-02-2002 19-06-2002
US 2001052798	A1	20-12-2001	CN WO EP JP	1381022 T 0175787 A1 1272974 A1 2003529855 T		20-11-2002 11-10-2001 08-01-2003 07-10-2003
US 6304613	B1	16-10-2001	EP WO JP	0993652 A1 9957681 A1 2002511972 T		19-04-2000 11-11-1999 16-04-2002
EP 0874327	A	28-10-1998	EP US	0874327 A2 6027029 A		28-10-1998 22-02-2000
EP 1094604	A	25-04-2001	FR EP JP US	2800214 A1 1094604 A1 2001189091 A 6437609 B1		27-04-2001 25-04-2001 10-07-2001 20-08-2002